

## Silicon NPN Power Transistors

S2055N

## DESCRIPTION

- With TO-3P(H)IS package
- High voltage,high speed
- Low collector saturation voltage
- Built-in damper diode

## APPLICATIONS

- Color TV horizontal output applications
- Color TV switching regulator applications

## PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

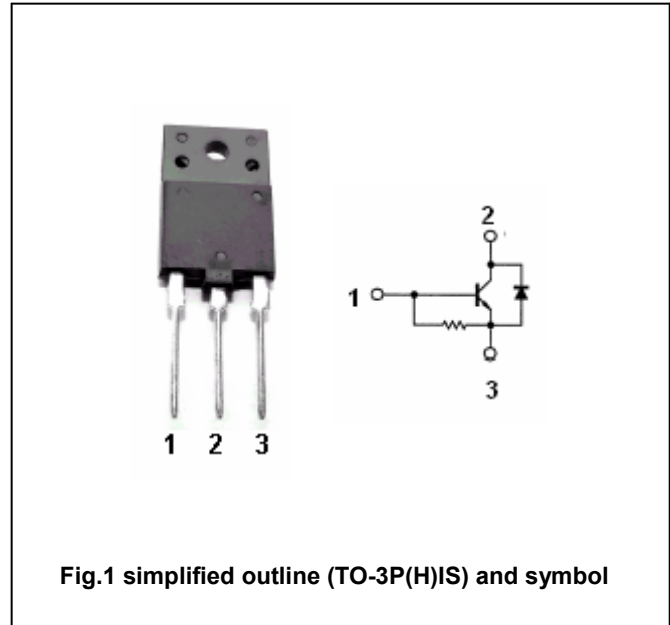


Fig.1 simplified outline (TO-3P(H)IS) and symbol

ABSOLUTE MAXIMUM RATINGS ( $T_C=25^\circ$ )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	1500	V
$V_{CEO}$	Collector-emitter voltage	Open base	700	V
$V_{EBO}$	Emitter-base voltage	Open collector	5	V
$I_C$	Collector current		8	A
$I_{CM}$	Collector current-peak		15	A
$I_B$	Base current		4	A
$P_C$	Collector power dissipation	$T_C=25^\circ$	50	W
$T_j$	Junction temperature		150	$^\circ$
$T_{stg}$	Storage temperature		-55~150	$^\circ$

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th\ j-C}$	Thermal resistance from junction to case	2.5	$^\circ/W$

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>B</sub> =500mA ; V <sub>BE</sub> =-1.7V; L=40mH	700			V
V <sub>CE(sat)-1</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =4.5A ; I <sub>B</sub> =2.0A			1.0	V
V <sub>CE(sat)-2</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =4.5A ; I <sub>B</sub> =1.0A			5.0	V
V <sub>BE(sat)</sub>	Base-emitter saturation voltage	I <sub>C</sub> =4.5A ; I <sub>B</sub> =1.0A			1.2	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =1500V; V <sub>BE</sub> =0			1.0	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =5V; I <sub>C</sub> =0			300	mA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =1A ; V <sub>CE</sub> =5V	10		30	
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =4.5A ; V <sub>CE</sub> =5V	4.5		9	
C <sub>OB</sub>	Collector output capacitance	I <sub>E</sub> =0 ; V <sub>CB</sub> =10V; f=1MHz		95		pF
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =0.1A ; V <sub>CE</sub> =10V		2		MHz

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PACKAGE OUTLINE

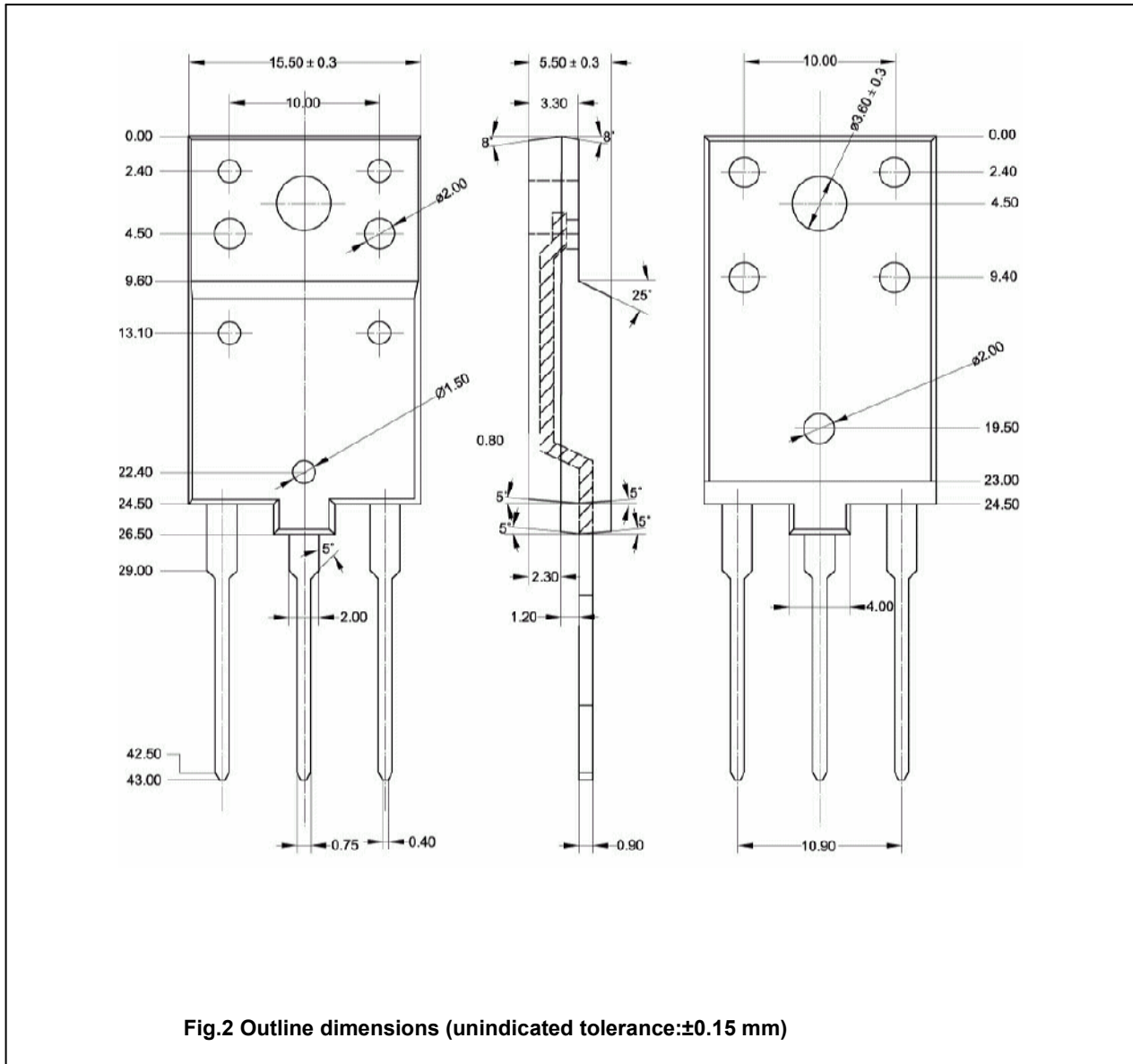


Fig.2 Outline dimensions (unindicated tolerance:  $\pm 0.15$  mm)