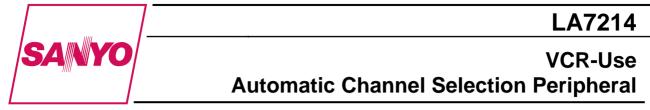
Monolithic Linear IC



Overview

The LA7214 is a VCR-use automatic channel selection peripheral IC that contains a sync separator, a video buffer, and a comparator.

Functions and Features

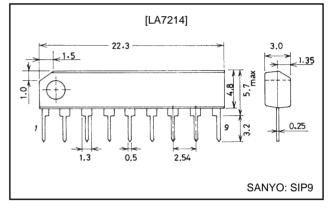
- Sync separator
- Video buffer
- Comparator
- Recommended supply voltage : 5V
- Sync separator, comparator output of open collector type (R_L=10kΩ), negative polarity output.

Specifications Maximum Ratings at Ta=25°C

unit: mm

Package Dimensions

3017C-SIP9



Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		7.0	V
Allowable power dissipation	Pd max	Ta≤75°C	150	mW
Operating temperature	Topr		-15 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

Operating Conditions at Ta=25°C

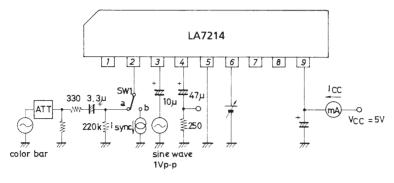
Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	VCC		5.0	V
Operating voltage range	ACC ob		4 to 6	V

Operating Characteristics at Ta=25°C, V_{CC}=5V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Circuit Current	ICC	No load	4	6.5	9	mA
Sync separation operating current	ISYNC	SW1=b		85		μA
Sync separation minimum input	V _I min	Color bar signal		-12		dB
level		1Vp-p=0dB, SW1=a				
Buffer amp gain	G ₁	RL=240Ω, VIN=1Vp-p, 500kHz	-2	-1	0	dB
Buffer amp gain	G ₂	R _L =240Ω, V _{IN} =1Vp-p, 5MHz	-2	-1	0	dB
Comparator reference voltage	V7			2.5		V
Hysteresis amount	ΔV_7	$\Delta V_7 = V_7 H - V_7 L$		50		mV
Output DC level	V _{1H}	No load	4.9			V
Output DC level	V _{1L}	No load			0.2	V
Output DC level	V _{8H}	No load	4.9			V
Output DC level	V _{8L}	No load			0.2	V

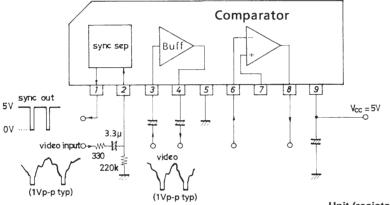
SANYO Electric Co., Ltd. Semiconductor Bussiness Headquarters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

Test Circuit

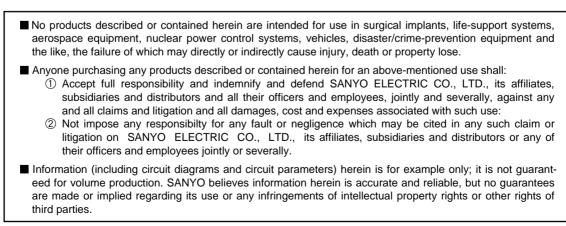


Unit (resistance : Ω , Capacitance : F)

Sample Application Circuit



Unit (resistance : Ω, Capacitance : F)



This catalog provides information as of July, 1997. Specifications and information herein are subject to change without notice.

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